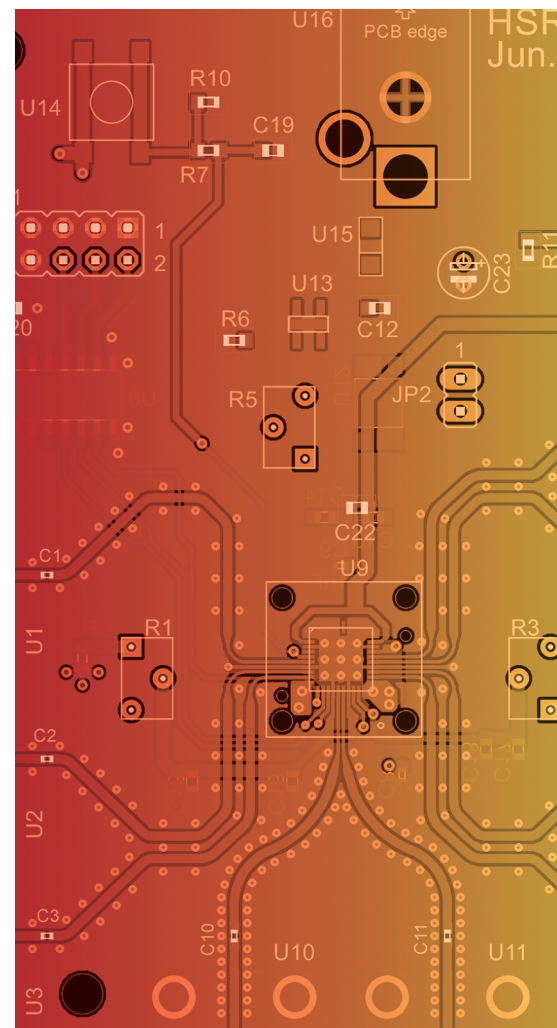




Workshop on Machine Learning for Hardware Design

Wednesday, October 18, onsite at the San Jose Doubletree Hotel following EPEPS

- 1:00pm - 1:15pm **Welcome Elyse Rosenbaum**
CAEML Center Director
- 1:15pm - 1:35pm **Dale Becker, IBM**
Machine Learning for High-Speed Channel Design
- 1:35pm - 1:55pm **Madhavan Swaminathan, GaTech**
Machine Learning and its Application
to Integrated Systems
- 1:55pm - 2:15pm **Tim Michalka, Qualcomm**
Machine Learning for Selected SI & PI Problems
- 2:15pm - 2:30pm **Break**
- 2:30pm - 2:50pm **Paul Franzon, NCSU**
Machine Learning and EDA
- 2:50pm - 3:10pm **Chris Cheng, HPE**
Machine Learning in System Design
- 3:10pm - 3:30pm **An-Yu Kuo, Cadence**
System Level ESD Simulation with Machine Learning
- 3:30pm - 3:50pm **Elyse Rosenbaum, Illinois**
Stochastic Modeling of Air Electrostatic Discharges
and Soft Failures in Consumer Electronics
- 3:50pm - 4:05pm **José Schutt-Ainé, Illinois**
Machine Learning with X Parameters for Behavioral
Model Synthesis



Information and Registration at <http://go.illinois.edu/caeml>

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